

Design Tools

System Settings



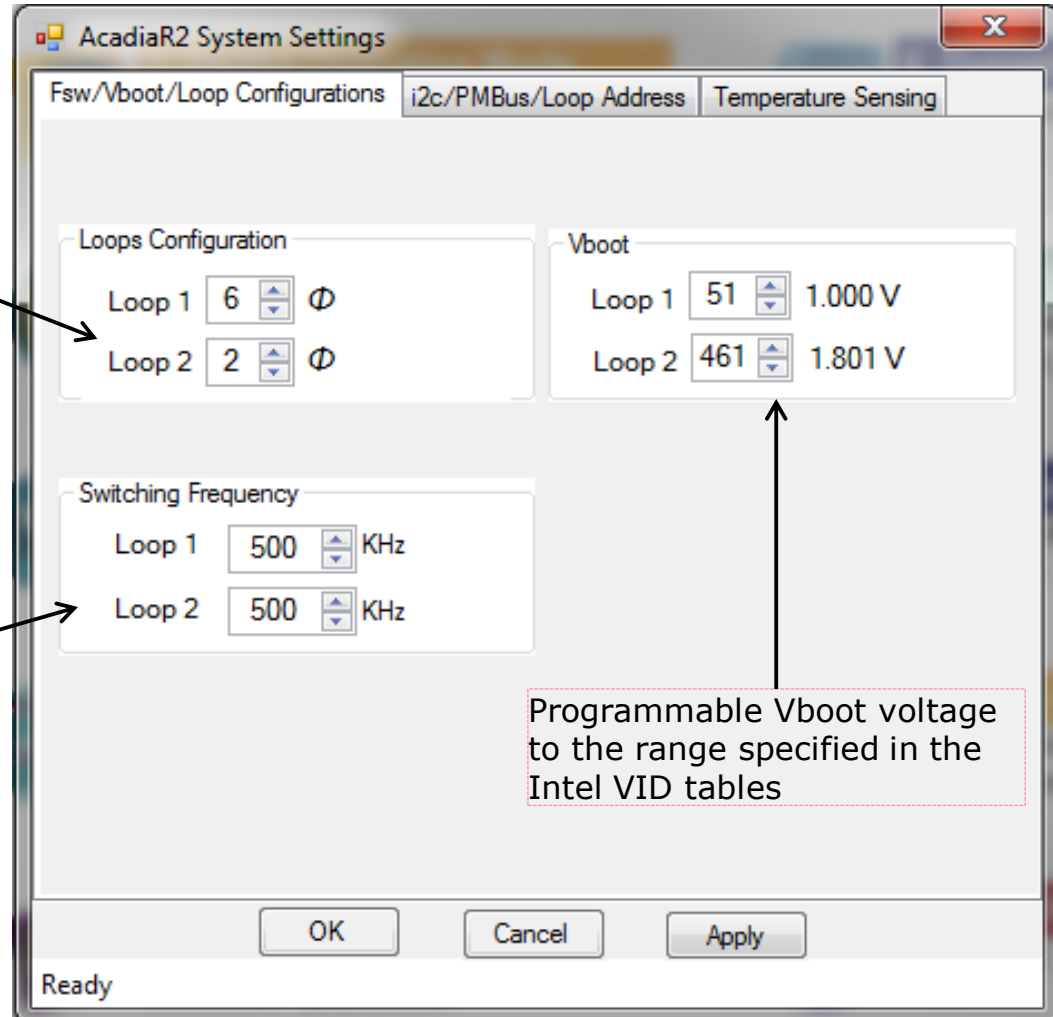
2. System Settings

Loop, I2C/PMBus Address, Vin, T Sense, Fsw

Used to configure number of phases in each loops, the switching frequency, the Vboot, and I2C/PMBus/loop address, and temperature sensing. Remember to press and **Apply** and **Ok** to program the settings.

Configure the number of phases for Loop1 and Loop2. The **max** number of phases is limited to **8** with **Loop1** able to control 1 to 8 phases and **Loop2** able to control 0 to 4 phases

Configurable switching frequency for each loop



The dialog box is titled "AcadiaR2 System Settings" and has three tabs: "Fsw/Vboot/Loop Configurations", "i2c/PMBus/Loop Address", and "Temperature Sensing". The "Fsw/Vboot/Loop Configurations" tab is active. It contains three main sections: "Loops Configuration", "Vboot", and "Switching Frequency".

- Loops Configuration:** Contains two rows. "Loop 1" has a spin box set to "6" and a phase symbol Φ . "Loop 2" has a spin box set to "2" and a phase symbol Φ .
- Vboot:** Contains two rows. "Loop 1" has a spin box set to "51" and a voltage field set to "1.000 V". "Loop 2" has a spin box set to "461" and a voltage field set to "1.801 V".
- Switching Frequency:** Contains two rows. "Loop 1" has a spin box set to "500" and a unit field set to "KHz". "Loop 2" has a spin box set to "500" and a unit field set to "KHz".

At the bottom of the dialog are three buttons: "OK", "Cancel", and "Apply". The status bar at the bottom left says "Ready".

Programmable Vboot voltage to the range specified in the Intel VID tables

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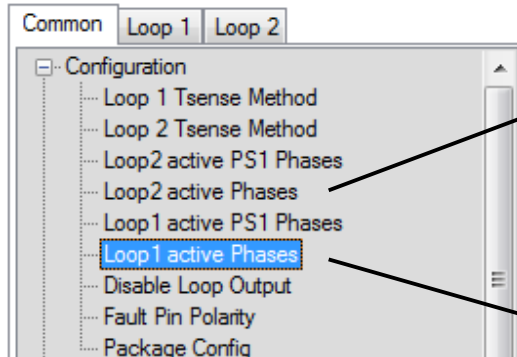
System Settings



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TSense, Fsw

System Settings configurations can also be programmed in **Register Map** using commands from the tree view. Remember to click **Write** to program the settings



loop_2_phase_active_max
x24 [5:4] 1 x01

2

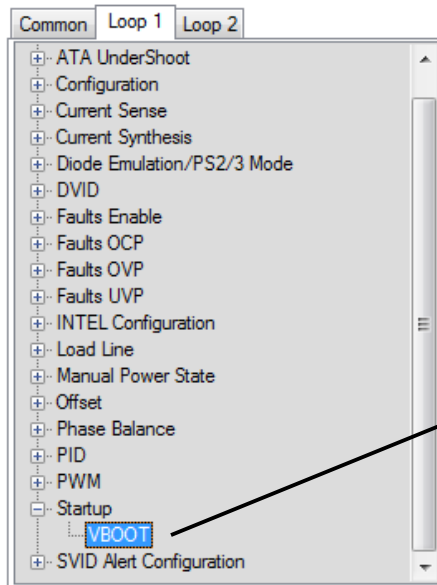
Read Write

loop_1_phase_active_max
x24 [2:0] 5 x05

6

Read Write

In Common section, under **Configuration** **Loop1 and Loop2 active phases** - configure the number of active phases for Loop1 and Loop2.



svid_vboot
x42C [15:8] 51 x33

1 V

Read Write

In Loop 1 or Loop 2 Section, under **Startup**

VBOOT - controls the Vboot voltage specified in Intel VID table.